



KEY FEATURES



12BB Half-cut Cell Technology

New circuit design, low current, lower R_s loss
Ga doped wafer, attenuation $\leq 2\%$ (1st year) / $\leq 0.45\%$ (Linear)



Industry Leading High Yield

Bifacial PERC cell technology,
5%-25% more yield depends on different conditions



Excellent Performance

Standard Anti-PID test



ELECTRICAL CHARACTERISTICS

Testing Condition	STC	NMOT	STC	NMOT	STC	NMOT	STC	NMOT	STC	NMOT
Maximum Power (Pmax/W)										
Operating Voltage (Vmpp/V)										
Operating Current (Impp/A)										
Open-Circuit Voltage (Voc/V)										
Short-Circuit Current (Isc/A)										
Module Efficiency (%)	20.70		20.80		21.00		21.20		21.40	

STC: Irradiance 1000W/m², Spectra at AM1.5, Module Temperature 25 C. Power output tolerance: 0~+5W, Test uncertainty for Pmax: ±3%
 NMOT: Irradiance 800W/m², Spectra at AM1.5, Ambient Temperature 20 C, Wind speed 1m/s

REAR SIDE POWER GAIN(REFERECE TO 595W FRONT)

Pmax gain	5%	10%	15%	20%	25%
Pmax/W	625	655	684	714	744
Vmpp/V	34.50	34.50	34.50	34.50	34.50
Impp/A	18.11	18.98	19.84	20.70	21.56
Voc/V	41.30	41.30	41.30	41.30	41.30
Isc/A	19.23	20.14	21.06	21.97	22.89

Cell Type	

Module Dimensions: 1950.00mm(L) x 914.00mm(W) x 35.00mm(H) Front Glass: 3.2mm Back Glass: 3.2mm Connectors: 4mm Pitch